

64K Real Time Trace/192 Bit Trace Width
68330 • Real Time Accessible Trace •
68331 • pSOS+ Aware • **PowerPC** • Open
Software Interface • **68340** • 64 Hardware
Breakpoints • **68341** • Scalable • **68332** •
8 Nested Levels **68360** • Ethernet Com-
munication • **68HC16** • 48 Bit Time Stamp

PowerPC • Selective Tracing • **68F333**
VRTX/OS Aware • **68331** • Parallel Interface
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visionICE: The Powerful Emulator With Scalable Performance

EST pushes the limits of price/performance with the new visionICE emulator. It has a unique scalable architecture which makes upgrading the entire system quick and simple. From low cost target control all the way up through full-scale emulation, visionICE can be customized to fit into any development strategy. The environment is constructed from multiple emulation modules which, used separately or assembled into a single unit, can exact any required level of support. Having a scalable architecture maximizes the system's price performance because emulator modules can be shared across any number of development seats to bring high-end features where you need them, when you need them. With visionICE your environment is never under powered, never over-priced.

visionEVENT: The Full Scale Emulation Module

visionEVENT is the state of the art emulation and logic analysis module that easily slides into the visionICE system. It features 64 hardware comparitors, 4 global event/trace counters and 8 external inputs. All of which can be combined into nested 'if...then' event equations and can selectively trigger a 64K x 192 bit trace buffer with a 48 bit time stamp. visionEVENT offers full speed emulation power that allows you to isolate and capture even the most evasive Real-Time bugs, without any target intrusion. Adding a second visionEVENT module to the EST environment can double the event and trace control system, or supply integrated logic analysis. Only visionICE delivers uncompromised debugging performance in a truly scalable, affordable, architecture.

68330

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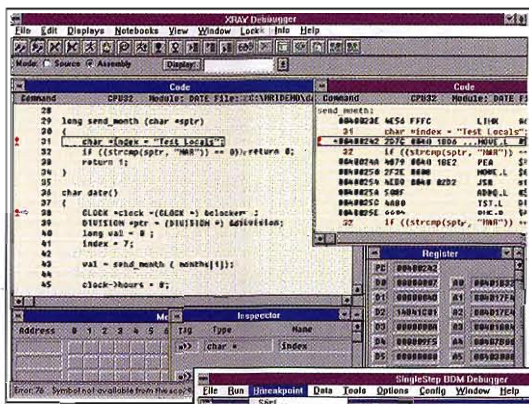
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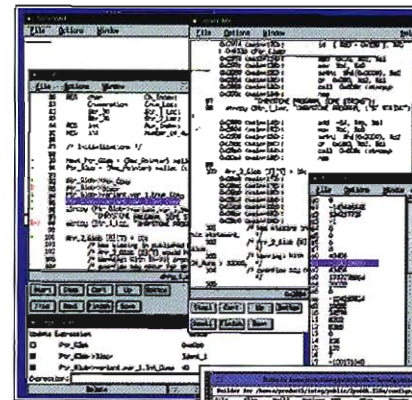
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PowerPC

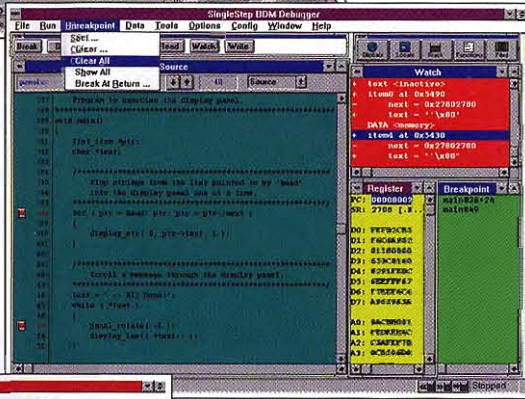


**XRAY/VRTX
by Microtec
Research**

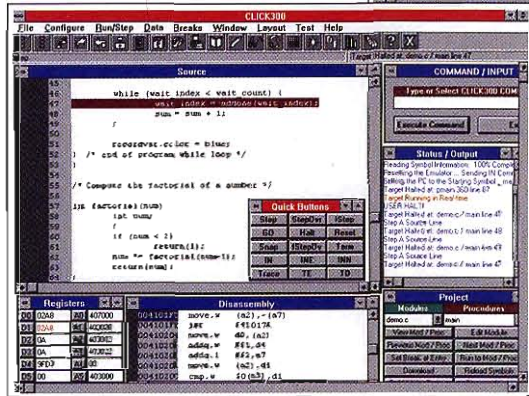
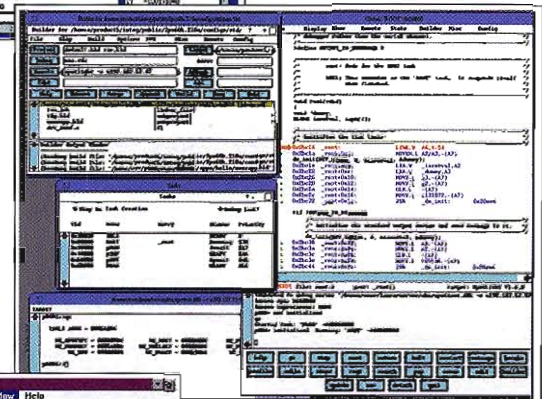


**GDB by
Cygnus Support**

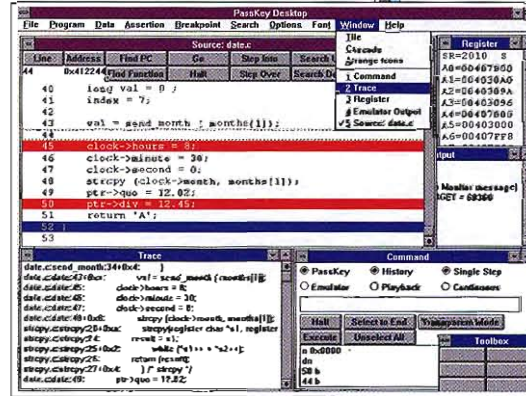
**Single-Step
by Software
Development
Systems**



**pSOSystem
by Integrated
Systems**



**CLICK300 by
EST Corp.**

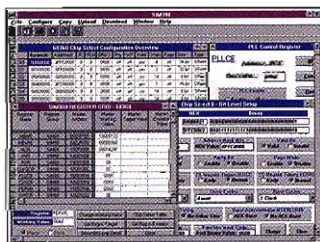


**PassKey by
Intermetrics
Microsystems**

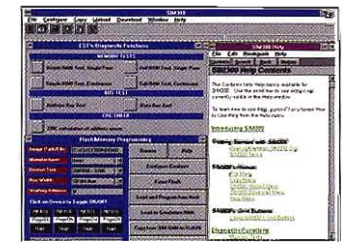
More Software Support For More Development Power

EST's scalable tool architecture extends beyond hardware components to include third-party software tool chains. visionICE has complete plug and play compatibility with top of the line products from Cygnus Support, Integrated Systems, Intermetrics Microsystems, Microtec Research, and Software Development Systems. Each tool chain brings crucial development power to your environment, like debugging optimized code, Kernel querying facilities, or a graphical user interface. But the big advantage to EST's software

compatibility is the increased productivity you'll gain from a single integrated environment. Now one interface can be used project wide for hardware debugging, board support and driver testing, C/C++ source-level debugging, and task aware system integration. Software engineers and hardware engineers alike can work in one environment, increasing your overall productivity and decreasing your time to market. visionICE delivers more power because it's more compatible.



Point-and-click SIM and chip select configuration software automates boot code generation



EST's graphical utilities include automated flash programming and hardware diagnostics

High End Emulation On A Whole New Scale

EST's new scalable architecture means your tools are never overpriced, never under-powered. See for yourself how EST's modular feature sets can help you complete your embedded project faster than ever before.

visionICE Feature Specifications By Module

visionCONTROL Specifications

Targets Supported	68330, 68331, 68332, 68F333, 68334, 68340, 68341, 68349, 68360, 68HC16, PowerPC (Q3 '95)
Communications	High speed serial and bi-directional parallel, standard.
Code Profiling	Integrated statistical performance analysis.
Flash Memory Programming	Supports flash programming algorithms for most 3, 5, and 12 volt devices. Optional 1, 2, or 4 Mbyte flash memory card for storing image in non-volatile memory.
Open Test	Provides an application programming interface for production test and repair diagnostics.

visionNET Specifications

visionNET Option	Provides ethernet communications for high speed download, shared resources and remote debugging.
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visionEVENT Specifications

Hardware Event System	64 (132 bit) hardware comparitors on 8 levels, 8 per level. Trigger on address, data, control, time stamp, and external signals, including ranges and mask values.
Pipe-Line Emulation	Eliminates breaking or triggering trace on fetched but non-executed instructions.
Event Counters	4 global event counters may be used with hardware comparitors in "if-then" event equations.
Real-Time Trace	64K x 192 bit or 128K x 96 bit. Configurable to trace on target clock or target bus cycle. Full speed, no-wait-state, up to 33 MHz.
Time Stamp	A 48 bit clock cycle stamp is included in real-time trace.
Trace Control and Filtering	Full selectable trace on/off after any event. Includes dedicated pre-event, post-event, and post trigger counters. Trace may be filtered and analyzed without stopping the target.
Multiple Event Cards	Supports 2 or more visionEVENT cards for double event system, trace width, or trace depth.
Logic Analyzer Mode	Provides 96 channels @ 25MHz or 48 channels @ 50 MHz or 24 channels @ 100 MHz.
Target Adapters and Overlay Memory	Supplied on pod assembly for QFP, PGA and other sockets. Also supports direct target connect options. Pod supports 1, 2, and 4 Mbytes optional overlay memory.

Fax or mail in this card today, or call 800-957-5588.

Yes, I'd like more information on visionICE.

- I have an immediate need, please call me.
- Please mail me all the current information on visionICE.
- I've already selected software tools.
- I need more information on supported software tools.



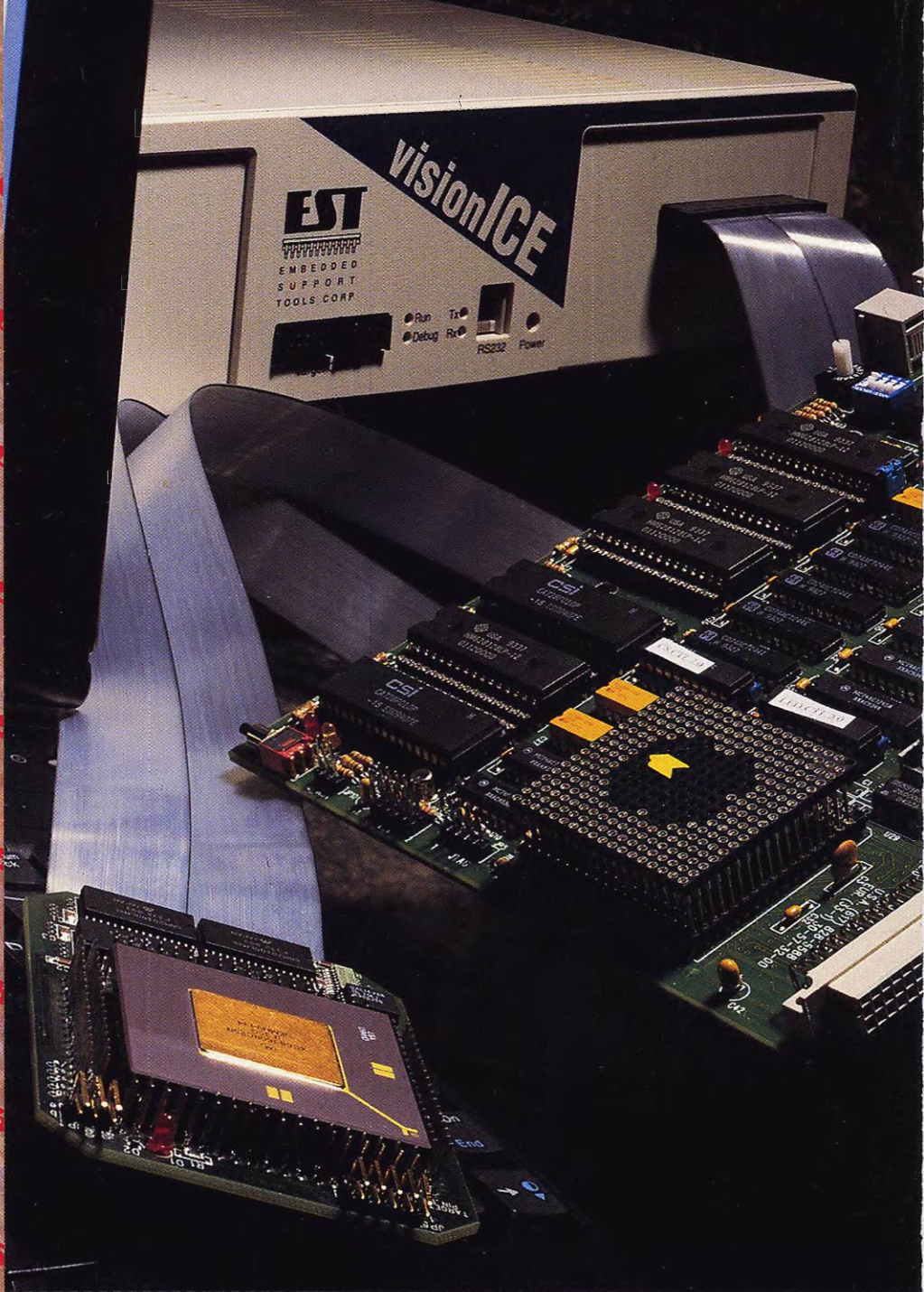
Name/Title _____

Company _____

Address _____

Phone _____ Fax _____

Target _____ Host _____



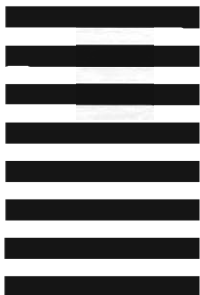
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